

Notice of References Cited	Application/Control No. 10/812,323	Applicant(s)/Patent Under Reexamination ANSARI, AHMAD R.	
	Examiner Richard Ellis	Art Unit 2183	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-4,268,907	05-1981	Porter et al.	711/138
*	B	US-5,911,151	06-1999	Circello et al.	711/201
*	C	US-5,255,378	10-1993	Crawford et al.	710/307
*	D	US-RE36,052	01-1999	Kobayashi et al.	710/307
*	E	US-6,330,623	12-2001	Wu et al.	710/23
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NON-PATENT DOCUMENTS

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	U	Getzlaff et al., IBM Technical Disclosure Bulletin, "Addressing of Storage Operands with Variable Field Length", Vol. 38, No. 5, May 1995.
	V	IBM Technical Disclosure Bulletin, "Method for Handling Unaligned Operands that Extend Across Memory Protection Boundaries in a Microprocessor", Vol. 37, Number 4B, April 1, 1994, pp. 597-598.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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